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unfolding an edge U→V : ØThe wordlength W is a multiple of the unfolding factor J, i.e. W = W'J. ØAll edges into and out of the switch have no delays. • With the above two assumptions an edge U→V can be unfolded as follows : ØWrite the switching instance as Wl + u = J(W'l + u/J) + (u%J)

Chapter 5: Unfolding

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Unfolding is a transformation technique of duplicating the functional blocks to increase the throughput of the DSP program in such a way that preserves its functional behavior at its outputs. Unfolding was first proposed by Keshab K. Parhi and David G. Messerschmitt in 1989. . Unfolding in general program is as known as Loop unrolling. Unfolding has applications in designing high-speed and low ...

Unfolding (DSP implementation) - Wikipedia

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Brief Biography. Keshab K. Parhi received the B.Tech. (Honors) degree from the Indian Institute of Technology, Kharagpur (India) in 1982 in Electrical Engineering, the M.S. degree from the University of Pennsylvania in 1984 in Electrical Engineering, and the Ph.D. degree from the University of California, Berkeley in 1988 in Electrical Engineering and Computer Sciences.

Keshab K. Parhi - Electrical and Computer Engineering

VLSI Architectures - Spring 2008. Course Syllabus . Course Objectives: The course will cover the most important methodologies for designing custom or semi-custom VLSI systems for some typical signal processing applications. General techniques covered include pipelining, retiming, folding and unfolding, and systolic array design.

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Chapter 4 Animal Farm Questions

Parhi is widely recognized for his work in the area of VLSI digital signal and image processing. In addition to publishing more than two hundred fifty papers and serving on the editorial boards of a number of professional journals, he has coauthored several books, most recently, Pipelined Lattice and Wave Digital Recursive Filters, and Digital ...

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Static rate-optimal scheduling of iterative data-flow programs via optimum unfolding. KK Parhi, DG Messerschmitt. IEEE Transactions on Computers, 178-195, 1991. 452: 1991: ... KC Lee, JG Chung, KK Parhi. IEEE Transactions on Very Large Scale Integration (VLSI) Systems 12 (5), 522-531, 2004.

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